

CLAIMS:

1. A method for manufacturing a multi-layered ceramic electronic component by laminating a plurality of multi-layered units each formed by laminating a ceramic green sheet, an electrode layer and a release layer on a support sheet in this order, the method comprising steps of positioning the multi-layered unit on a base substrate so that the surface of the release layer of the multi-layered unit is contact with an agglutinant layer formed on the surface of the base substrate in such a manner that the bonding strength between itself and the support substrate is higher than the bonding strength between the support sheet and the ceramic green sheet and lower than the bonding strength between itself and the release layer, pressing it and laminating multi-layered units on the base substrate.
- 15 2. A method for manufacturing a multi-layered ceramic electronic component in accordance with Claim 1, wherein the agglutinant layer has a thickness of 0.01 μm to 0.3 μm .
- 20 3. A method for manufacturing a multi-layered ceramic electronic component in accordance with Claim 1, wherein the release layer contains a binder belonging to the same binder group as that a binder contained in the ceramic green sheet belongs to and the agglutinant layer contains a binder belonging to the same binder group as that the binder contained in the release layer belongs to.
- 25 4. A method for manufacturing a multi-layered ceramic electronic component in accordance with Claim 2, wherein the release layer contains a binder belonging to the same binder group as that a binder contained in

the ceramic green sheet belongs to and the agglutinant layer contains a binder belonging to the same binder group as that the binder contained in the release layer belongs to.

5 5. A method for manufacturing a multi-layered ceramic electronic component in accordance with Claim 1, wherein the release layer contains a plasticizing agent belonging to the same plasticizing agent group as that a plasticizing agent contained in the ceramic green sheet belongs to and the agglutinant layer contains a plasticizing agent belonging to the 10 same plasticizing agent group as that the plasticizing agent contained in the release layer belongs to.

6. A method for manufacturing a multi-layered ceramic electronic component in accordance with Claim 2, wherein the release layer contains a plasticizing agent belonging to the same plasticizing agent group as that a plasticizing agent contained in the ceramic green sheet belongs to and the agglutinant layer contains a plasticizing agent belonging to the same plasticizing agent group as that the plasticizing agent contained in the release layer belongs to.

20

7. A method for manufacturing a multi-layered ceramic electronic component in accordance with Claim 1, wherein the release layer contains dielectric particles having the same composition as that of dielectric particles contained in the ceramic green sheet and the agglutinant layer 25 contains dielectric particles having the same composition as that of dielectric particles contained in the release layer.

8. A method for manufacturing a multi-layered ceramic electronic

component in accordance with Claim 2, wherein the release layer contains dielectric particles having the same composition as that of dielectric particles contained in the ceramic green sheet and the agglutinant layer contains dielectric particles having the same composition as that of dielectric particles contained in the release layer.

9. A method for manufacturing a multi-layered ceramic electronic component in accordance with Claim 1, wherein the agglutinant layer contains an ampholytic surfactant in an amount smaller than that of the binder.

10. A method for manufacturing a multi-layered ceramic electronic component in accordance with Claim 2, wherein the agglutinant layer contains an ampholytic surfactant in an amount smaller than that of the binder.

11. A method for manufacturing a multi-layered ceramic electronic component in accordance with Claim 1, wherein the base substrate is formed of a plastic material selected from a group consisting of polyethylene, polypropylene, polycarbonate, polyphenylene ether and polyethylene terephthalate.

12. A method for manufacturing a multi-layered ceramic electronic component in accordance with Claim 2, wherein the base substrate is formed of a plastic material selected from a group consisting of polyethylene, polypropylene, polycarbonate, polyphenylene ether and polyethylene terephthalate.

13. A method for manufacturing a multi-layered ceramic electronic component in accordance with Claim 1, wherein the ceramic green sheet has a thickness equal to or thinner than 3 μm .

5 14. A method for manufacturing a multi-layered ceramic electronic component in accordance with Claim 2, wherein the ceramic green sheet has a thickness equal to or thinner than 3 μm .

10 15. A method for manufacturing a multi-layered ceramic electronic component in accordance with Claim 1 which further includes steps of peeling off the support sheet from the ceramic green sheet of the multi-layered unit laminated on the base substrate and further laminating a new multi-layered unit in which an adhesive layer is formed on the surface of a release layer onto the ceramic green sheet of the 15 multi-layered unit laminated on the base substrate via the adhesive layer.

20 16. A method for manufacturing a multi-layered ceramic electronic component in accordance with Claim 1 which further includes steps of peeling off the support sheet from the ceramic green sheet of the multi-layered unit laminated on the base substrate and further laminating a new multi-layered unit in which an adhesive layer is formed on the surface of a release layer onto the ceramic green sheet of the 25 multi-layered unit laminated on the base substrate via the adhesive layer.

25 17. A method for manufacturing a multi-layered ceramic electronic component in accordance with Claim 1, wherein the multi-layered unit includes a spacer layer formed on the surface of the release layer in a complementary pattern to that of the electrode layer.

18. A method for manufacturing a multi-layered ceramic electronic component in accordance with Claim 2, wherein the multi-layered unit includes a spacer layer formed on the surface of the release layer in a
5 complementary pattern to that of the electrode layer